

PATENT APPLICATION
Docket No. 9903-071
Client No. S02US035

22264 U.S. PTO
10/690782



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: MAIL STOP PATENT APPLICATION, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450.

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Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Jin-Hyuk LEE, Gu-Sung KIM, Dong-Ho LEE and Dong-Hyeon JANG

For: METHOD FOR MANUFACTURING A WAFER LEVEL CHIP SCALE PACKAGE

Applicant requests FIG. 13 to be published with the application.

Enclosures:

- ☒ Specification (pages 1-10); claims (pages 11-13); abstract (page 14)
- ☒ TEN sheet(s) of FORMAL drawings (comprising 21 figures)
- ☒ Certified copy of Priority Document No. 2002-64513, filed October 22, 2002, from which priority is claimed.
- ☒ PTO 1449 Information Disclosure Statement with 6 cited references.

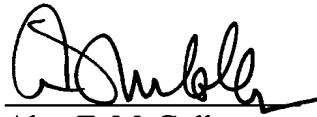
CLAIMS AS FILED				
For	Number Filed	Number Extra	Rate	Basic Fee
Total Claims	17-20	0	x \$ 18 =	\$0.00
Independent Claims	3-3	0	x \$ 86 =	\$0.00
TOTAL FILING FEE				\$770.00

- ☒ Combined Declaration and Power of Attorney
☒ Newly executed (original or copy)
☐ Assignment with cover sheet
Assignee Name and Address: Samsung Electronics Co., Ltd.
Suwon-city, Kyungki-do Republic of Korea
- ☒ Return Postcard
☒ PTO Form 2038 authorizing credit card payment of \$770.00 (filing fee) is enclosed.
☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

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Respectfully submitted,

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